

Biography

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Dr Chris Dick is the Manager of the Signal Processing Group at Xilinx Inc. He is responsible for coordinating the DSP intellectual property engineering activities and is the DSP Chief Architect at Xilinx.

Dr Dick joined Xilinx in 1997 from La Trobe University in Melbourne Australia where he was a professor for 13 years. Chris has over 60 journal and conference publications and has been an invited speaker at many international DSP and communications symposiums.

Chris' research interests are in the areas of fast algorithms for signal processing, digital communication, software defined radios, hardware architectures for real-time signal processing, parallel computing, interconnection networks for parallel processors, and the use of Field Programmable Arrays (FPGAs) for custom computing machines and signal processing. Chris has published many papers in the fields of signal processing, parallel computing, Inverse Synthetic Array Radar (ISAR) imaging, and the use of FPGAs for building computing platforms for signal processing applications.

He holds a bachelor's and PhD degrees in the areas of computer science and electronic engineering.